

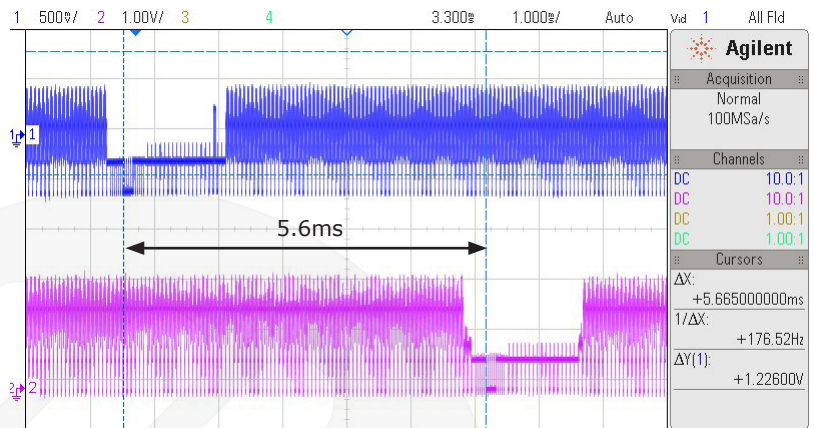
**Product Brief**

**ITAVA Live-CoDec 580 ULD Product Line**

*„remote control with the live feeling“*

With the unique Live-CoDec 580 ULD Product Line ITAVA Systems developed a coding system which allows video transmissions with a non-visible subframe-latency using common wireless links.

The IP-Core helps system designers to create flexible and mobile platforms with minimum risk using up-to-date technologies.



Measurement of delay between video input (top) and decoding output (bottom) using ASI serial link = 5.6ms

**Features**

- PAL/NTSC resolution
- CBR and VBR support
- Low start-up time

**Applications**

- UAV and UGV control
- Wireless video devices
- Video surveillance systems

**Performance**

- Fits in low-cost FPGAs
- Low timing requirements
- Data rates 5 to 30 MBit/s

**Deliverables**

- Post-synthesis EDIF/NGC netlist
- VHDL/Verilog Source Code
- Comprehensive user guide

**Purchase and Technical Support**

An ASI-connected FPGA demonstrator system is available on request. The IP-Core is customizable to customers interfaces and implementation support is available.